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FOR IMMEDIATE RELEASE

Cypress Showcases Communications Solutions at DAC

Design Automation Conference (DAC)
Las Vegas Convention Center
Booth # 3437
Las Vegas, Nevada
June 18 - 22, 2001

San Jose, California, June 13, 2001 – Cypress Semiconductor (NYSE: CY) will display how it is "Driving the Programmable Communications Revolution" at the 38th annual Design Automation Conference (DAC). Cypress will highlight its integrated, programmable communications solutions, which are the result of the synergy between its competencies in programmable logic and high-speed physical layer (PHY) devices. Product presentations and PlayStation®2 giveaways will be conducted every hour in the Cypress booth #3437. DAC will be held on June 18-22, 2001 at the Las Vegas Convention Center in Las Vegas, Nevada.

Cypress is the first company to deliver a 2.5 Gb/s Programmable PHY product in its Programmable Serial Interface™ (PSI™) family of products. These devices combine Cypress's advanced-PHY technology with the flexibility, predictable timing, and ease-of-use of Cypress CPLDs as well as communication memory and phase-locked loops (PLLs). Cypress's *Warp*™ software enables a seamless programming interface to allow design engineers to easily integrate custom IP with the PHY via HDL text, schematic blocks, or graphical state machines.

In addition to showcasing PSI, Cypress will highlight their popular portfolio of Complex Programmable Logic Devices (CPLDs). The Delta39K™ family offers up to 350,000 gates to support high-performance communications applications. Cypress offers "CPLDs at FPGA Densities"™ with its Delta39K devices.

Cypress will feature live demonstrations and displays focusing on its programmable communications solutions:

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- The Programmable Serial Interface family combines high-speed serial communications Physical Layer devices; the speed, ease of use, and predictable timing of a CPLD; and abundant communications memory into a single, flexible solution.
- Cypress's W*arp*® Release 6.1 software is the first complete suite of design tools to include design entry, synthesis, fitting, and simulation packaging for PHYs to enable high-level system debugging.
- Delta39K™ devices are "CPLDs at FPGA Densities"™, with up to 350K gates (5000+ macrocells) of high-performance CPLD logic. These products offer abundant on-chip memory with embedded FIFO control, flag logic, and an unparalleled combination of features and I/O capabilities to enable true System-on-Chip (SoC) design.
- Quantum38K™ devices are "CPLDs at ASIC Prices"™ with up to 100K gates (up to 1535 macrocells) of high-performance logic. These products offer a low power operation, a simple timing model, and an In-System Reprogrammable™ (ISR™) feature allowing ease of design changes.
- The Ultra37000™ CPLDs are designed for ease-of-use with simple timing and excellent routability to provide true In-System-Reprogrammability (ISR). Their efficient architecture enables high gate utilization, and their high I/O count supports communications applications.

About Cypress

Cypress Semiconductor is "Driving the Communications Revolution"™ by providing high-performance integrated circuit solutions to fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial control. With a focus on emerging communications applications, Cypress's product portfolios include high-speed data communications ICs; networking-optimized and micropower static RAMs; high-bandwidth multi-port and FIFO memories; high-density programmable logic devices; timing technology solutions; and controllers for Universal Serial Bus (USB).

More than two-thirds of Cypress's sales come from fast-growing communications markets and dynamic companies such as Alcatel, Cisco, Ericsson, Lucent, Motorola, Nortel Networks, and 3Com. Cypress's ability to mix and match its broad portfolio of intellectual property enables targeted, integrated solutions for high-speed systems that feed bandwidth-hungry Internet applications. Cypress aims to become the preferred silicon supplier for Internet switching systems and for every Internet data stream to pass through at least one Cypress IC.

Cypress employs more than 4,700 people worldwide with international headquarters in San Jose, California. Its shares are listed on the New York Stock Exchange under the symbol CY. More

information about Cypress is accessible electronically on the company's worldwide Web site at http://www.cypress.com.

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"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties, including by not limited to: the effect of global economic conditions, shifts in supply and demand, market acceptance, the impact of competitive products and pricing, product development, commercialization and technological difficulties, and capacity and supply constraints. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

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